HDRMX #1 2025-03-19

DATA CAPTURE AND

CORRECTION FOR JUNGFRAU 9M

ACKNOWLEDGEMENTS

- SLS detector software stack
- Team at Diamond: Nick Devenish, James O'Hea, Gary Yendell, John for data capture)
- BBSRC for support to purchase detector / data acquisition system

Lots of useful conversations with Filip / Aldo / others at PSI detector group

Matheson, ... (many others involved in the project, primarily these people

JF9M CHALLENGE

- 9M detector @ 2kHz / 16 bit readout -36GB/s
- Data need to be corrected for perpixel / per gain mode pedestal and gain
- Data need to be shuffled and compressed (~6:1) then saved to disk
- Process needs to be steady state i.e. run continuously (data veto needed)



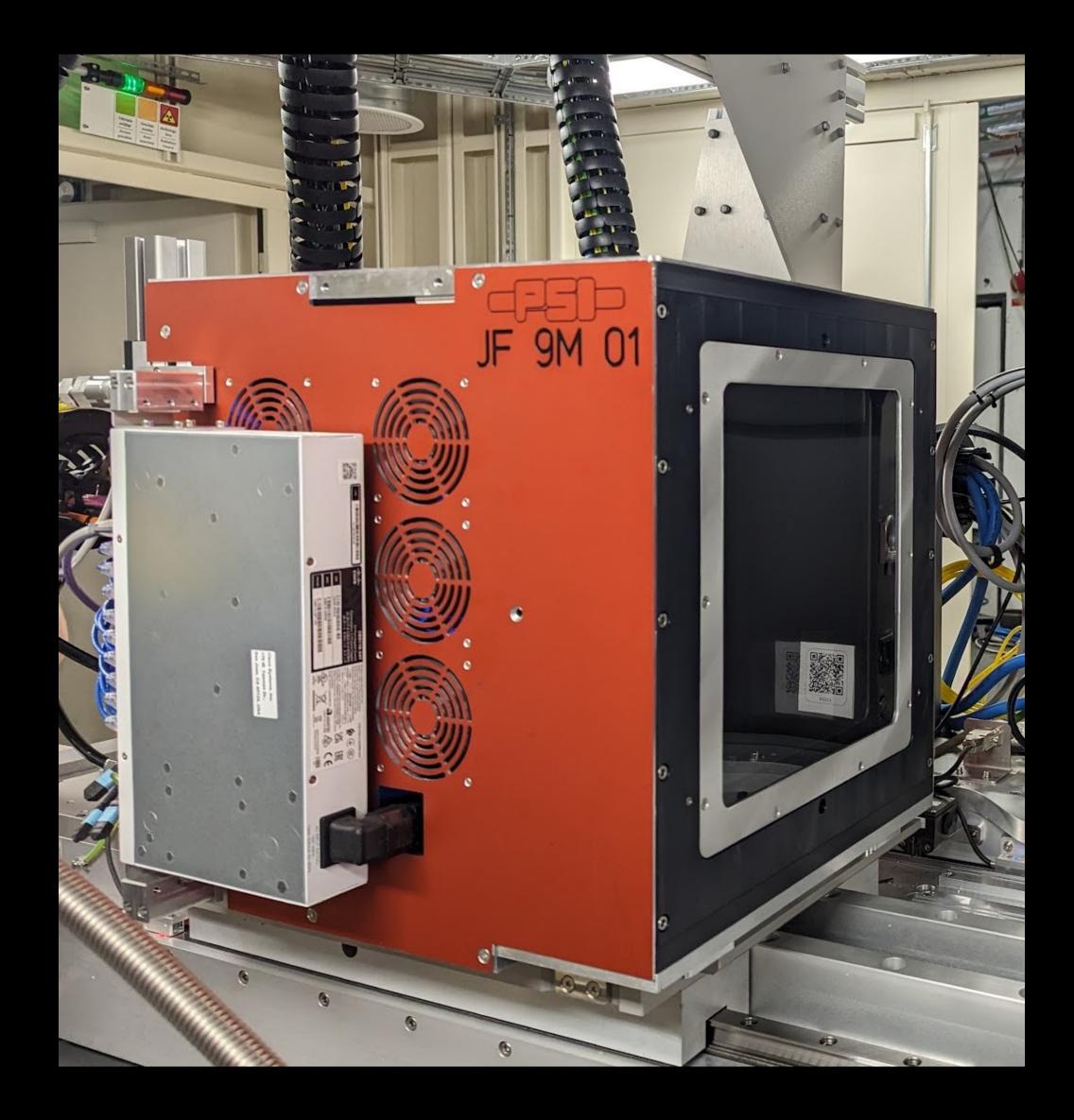


Image kindly provided by Aldo Mozzanica (PSI detector group)

PSI SOLUTION

- JUNGFRAUJOCH certain Filip will have described
- Limiting factors: memory bandwidth, real-time requirement
- FPGA HLS is an interesting technology but time consuming to implement and slow to debug

NVIDIA GRACE HOPPER **NVIDIA Grace Hopper Superchip** CPU LPDDR5X **GPU HBM3** ≤ 512 GB ≤ 96 GB HBM3 ≤546 GB/s ≤3000 GB/s HIGH-SPEED 1/0 NVLINK C2C 900 GB/s **4**x GRACE HOPPER 16x PCIe-5 CPU GPU 512 GB/s **CPU LPDDR5X** GPU HBM3 ≤ 96 GB HBM3 ≤ 512 GB

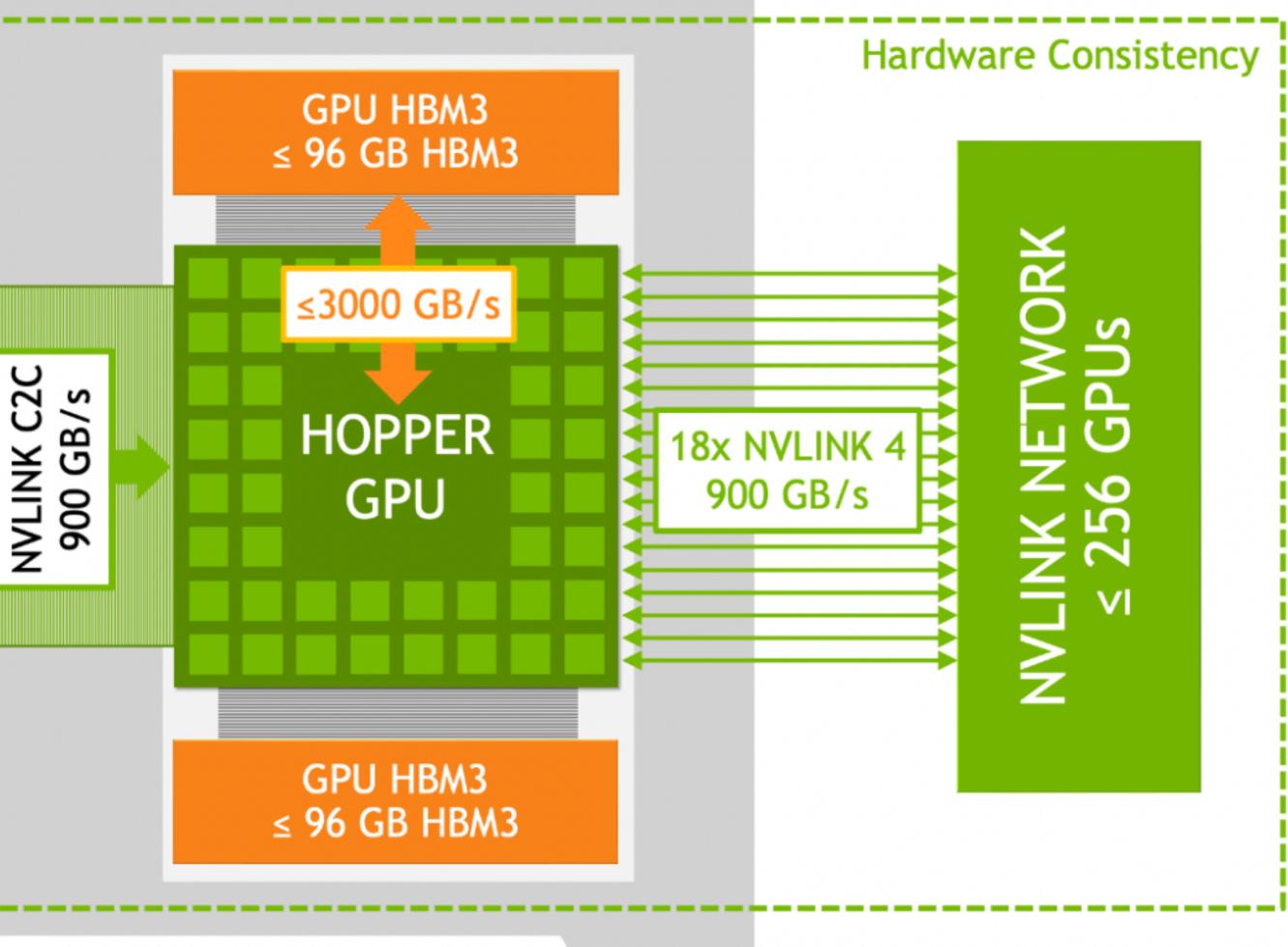


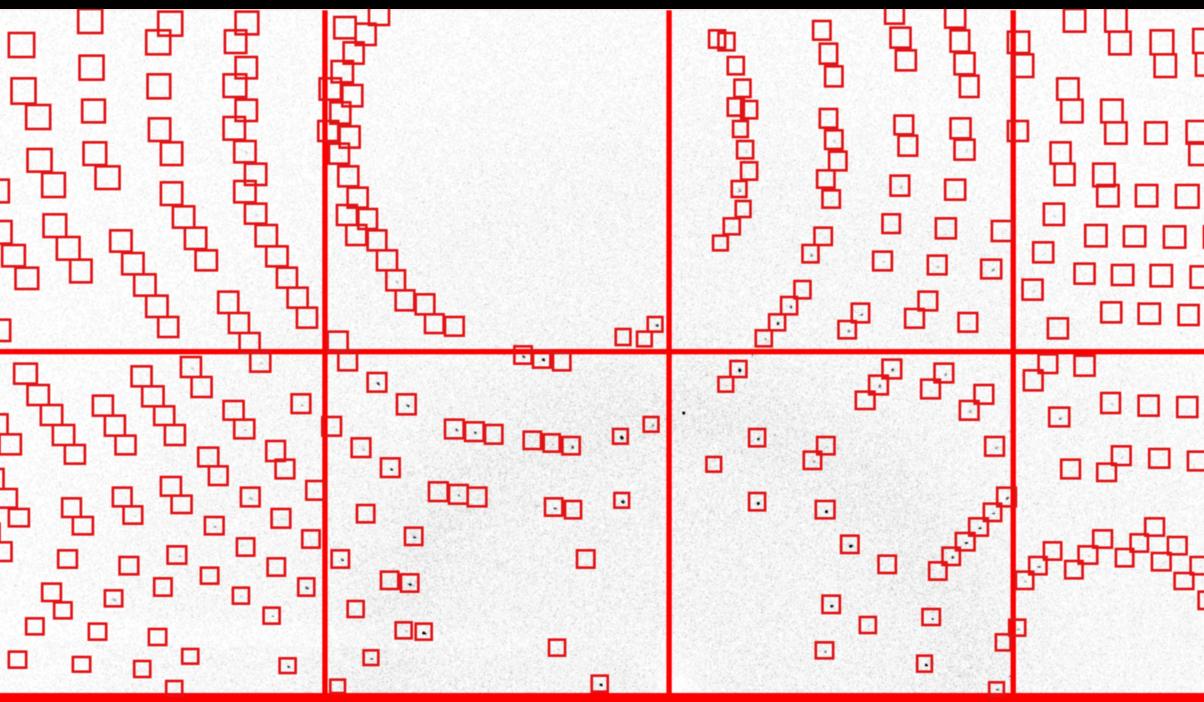
Diagram reproduced with permission of NVIDIA

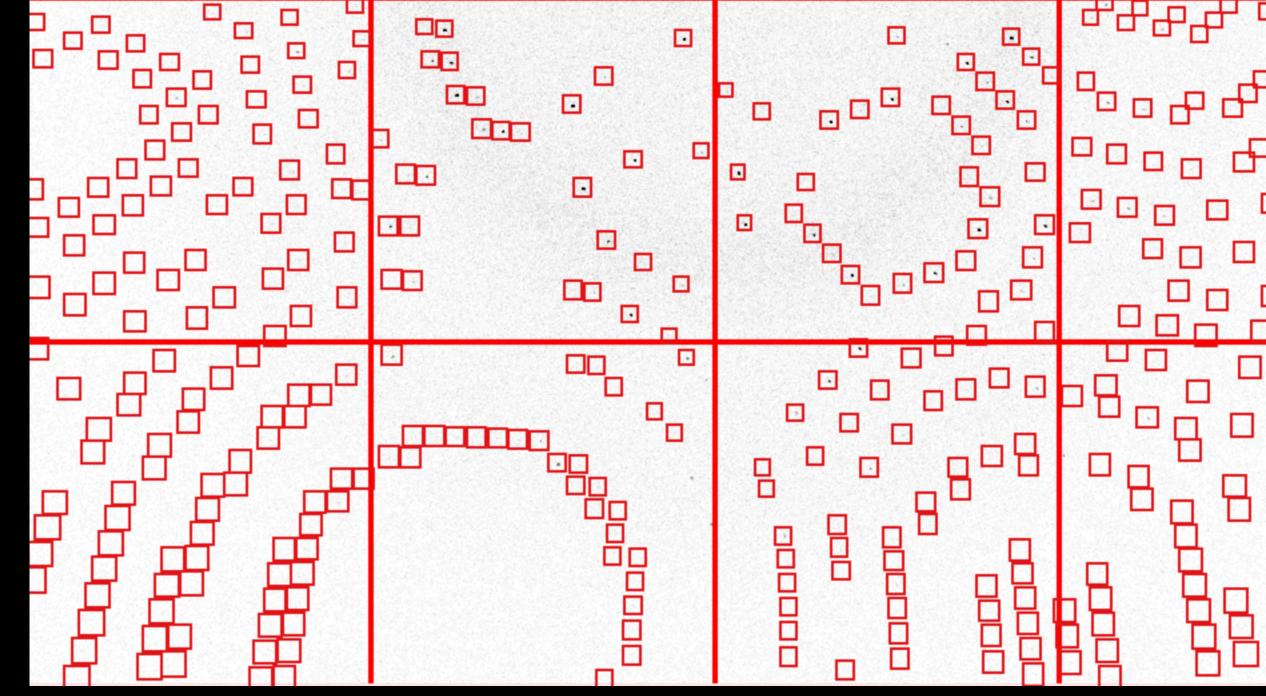
DIAMOND SOLUTION

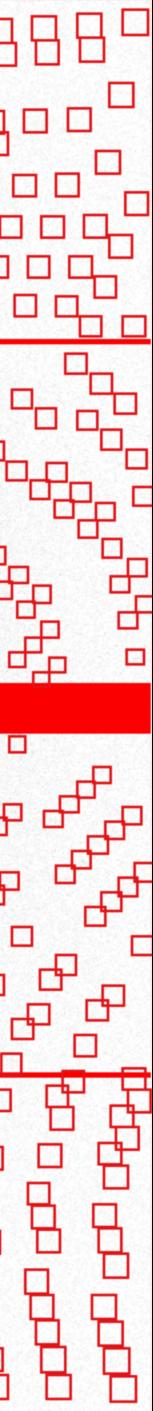
- SLS detector to receive packets from detector, form images
- Push data to GPU: perform correction in there, shuffling, maybe compression, maybe initial analysis
- Keeping the data streams from half-modules separated
- Save data to 36 x HDF5 stacks

COMMENTARY

- Compression: bslz4 uses 8kB chunks: splitting into modules does not reduce efficiency
- 36 half-modules can be reconstructed into one image using HDF5 VDS
- Never bringing data together can aid parallelism in analysis

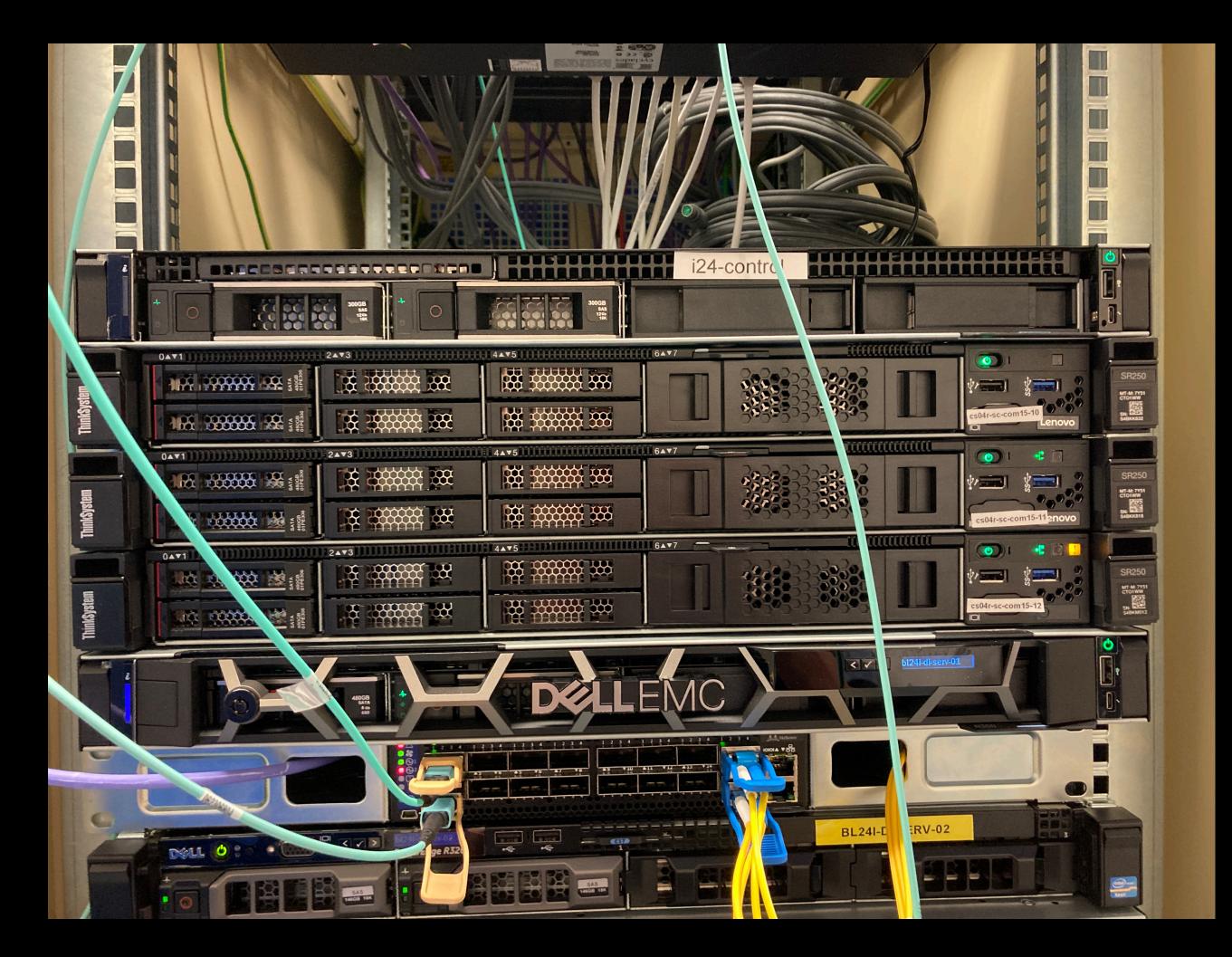






SIMULATION ENVIRONMENT

- 5 servers with 40GbE network 1 x single port, 4 x dual port
- 18 x virtual jungfrauDetectorServer (lightly hacked to send real data)
- SN2100 network switch (needed for final detector)
- Very long slsDetector configuration file



CURRENT WORK

- MORGUL
- speed for an hour at a time requires reliability
- Tuning the implementation

Optimisation: removing the zeroMQ connection between slsReceiver /

• Tuning the simulation environment: sending UDP packets at close to line

FUTURE WORK

- Example data from 1M: <u>https://zenodo.org/records/15017658</u>
- Exploring other applications of Grace Hopper

Optimise analysis around use of VDS (works in DIALS, not in XDS / DURIN)